DRM PTO-1083

RE application of S. KONDO et al

Serial No.: 09/637,570 <

Filed: August 14, 2000

PATENT

Case Docket No.

Group Art Unit: 2812

Examiner: Ha T. Nguyen

For: POLISHING METHOD, METALLIZATION FABRICATION METHOD, METHOD

FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DE

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment in the above-identified application.

Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.

A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.

No additional fee is required.

The fee has been calculated as shown below:

(COL. 1)			(C	(COL. 2)		(COL. 3)	
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If the entry in Col. 1 is less than the entry in Col. 2, write '0' in Col. 3.

If the 'Highest Number Previously Paid For' IN THIS SPACE is less than 20, write '20' in this space.

If the 'Highest Number Previously Paid For' IN THIS SPACE is less than 3, write '3' in this space.

The 'Highest Number Previously Paid For' (Total or Independent) is the highest number found from the equivalent box in Col. 1 of a prior Amendment or the number of claims originally filed.

	Please charge my Deposit Account No. 50-1417 in the amount of \$
	A check in the amount of \$ is attached in payment of:
х	The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1417.

Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

Any patent application processing fees under 37 CFR 1.17.

Any Extension of Time fees that are necessary, which are hereby requested if necessary.

MATTINGLY, STANGER & MALUR, P.C. 1800 Diagonal Rd., Suite 370 Alexandria, Virginia 22314 (703) 684-1120

Date: August 26, 2003

Registration No. Attorney for Applicant(s)

30,293

IN THE UNITED STATES PATENT AND TRADEMARK OFFIC

In re Patent Application of

s. KONDO et al

Serial No. 09/637,570 Group Art Unit: 2812

Filed: August 14, 2000 Examiner: Ha T. Nguyen

For: POLISHING METHOD, METALLIZATION FABRICATION

METHOD, METHOD FOR MANUFACTURING SEMICONDUCTOR

DEVICE AND SEMICONDUCTOR DEVICE

AMENDMENT

Commissioner for Patents Alexandria, VA 22313

Sir:

In response to the Office Action dated May 29, 2003, please amend the above-identified application as follows.

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